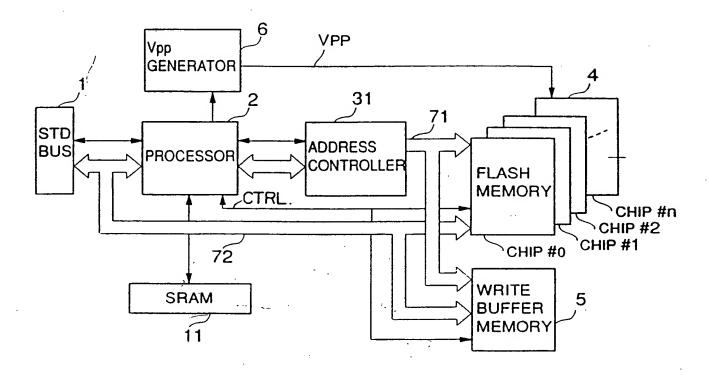
FIG. 1



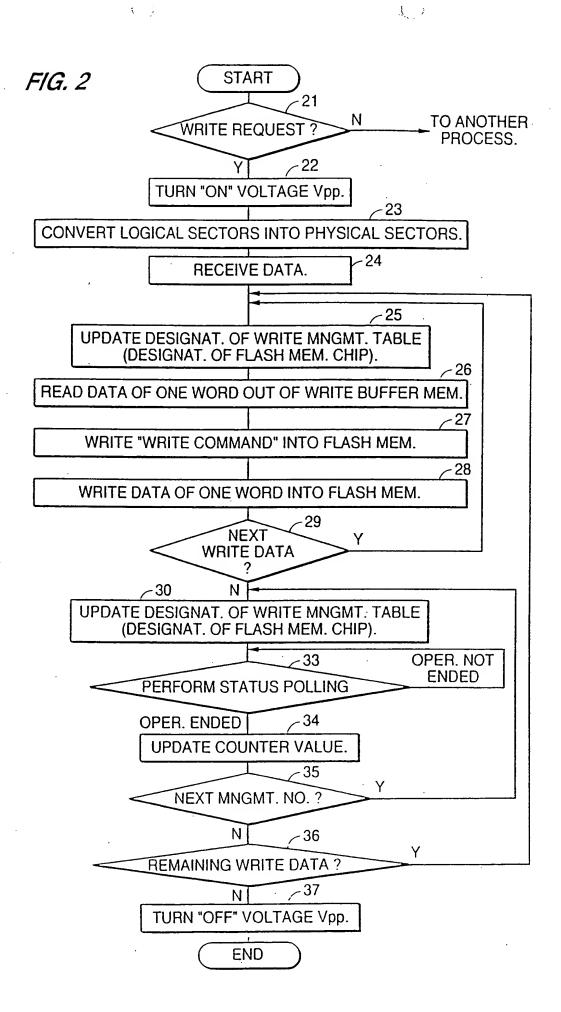


FIG. 3

MANAGE- MENT NO.	WRITE BUFFER MEMORY	FLASH MEMORIES (PHYSICAL SECTOR NOS.)		COUNTER
	BLOCK NO.	CHIP NO.	SECTOR NO.	
0	1	0	3	0
1	2	1	2	0
2	3	2	7 .	0
3	0	0	0	0

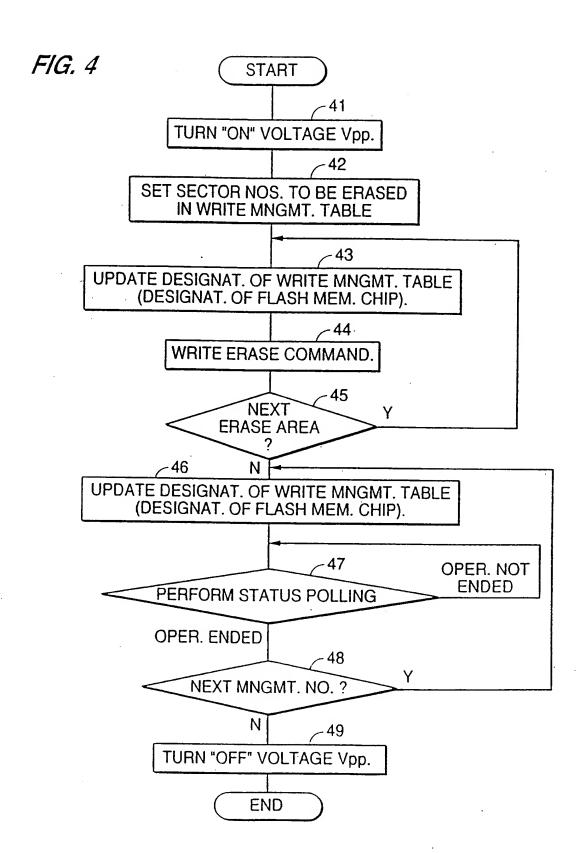


FIG. 5

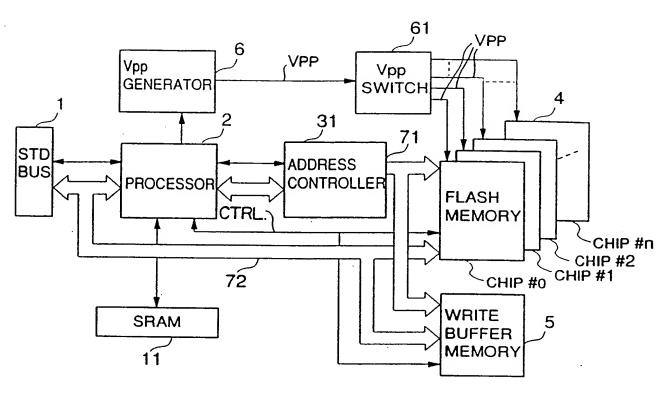


FIG. 6

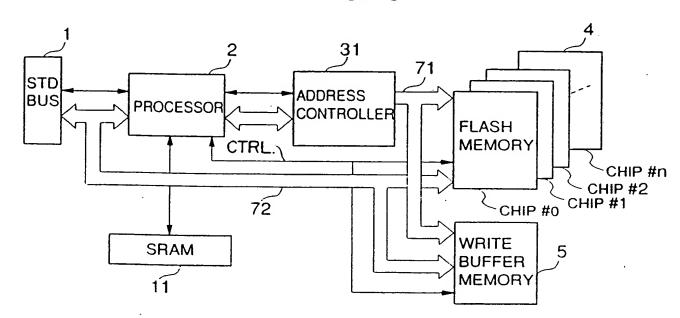


FIG. 7

